Ordering Information

List of Models

Non-Contact Door Switch Controllers

Safety outputs *1			Logical	Logical				
Instantaneous	OFF-delayed *2	Auxiliary outputs *3	AND connection input	AND connection output	Max. OFF delay time *4	Rated voltage	Terminal block type	Model
2 (Semi- conductors)	0	2 (Semi- conductors)	1	1		24 VDC	Screw terminals	G9SX-NS202-RT
							Spring-cage terminals	G9SX-NS202-RC
	2 (Semiconductors)				3.0 s		Screw terminals	G9SX-NSA222-T03-RT
							Spring-cage terminals	G9SX-NSA222-T03-RC

^{*1.} P channel MOS FET transistor output

Expansion Units

Safety outputs		Auxiliary	OFF delevitime	Rated	Tarminal black from	Model
Instantaneous	OFF-delayed	outputs	OFF-delay time	voltage	Terminal block type	Wodei
4PST-NO		1 (Semiconductor) *1		24 VDC	Screw terminals	G9SX-EX401-RT
					Spring-cage terminals	G9SX-EX401-RC
	4PST-NO		*2		Screw terminals	G9SX-EX041-T-RT
					Spring-cage terminals	G9SX-EX041-T-RC

^{*1.} PNP transistor output

Accessories

Terminal Block

Appearance *	Specifications	Applicable units	Model	Remarks
	Terminal Block with screw terminals (3-pin)	G9SX-NSA	Y9S-03T1B-02A	Two Terminal Blocks (black) with screw terminals, and a set of six code marks to prevent erroneous insertion.
999	Terminal Block with screw terminals (4-pin)	G9SX-NS G9SX-EX-□	Y9S-04T1B-02A	Two Terminal Blocks (black) with screw terminals, and a set of six code marks to prevent erroneous insertion.
	Terminal Block with spring- cage terminals (3-pin)	G9SX-NSA	Y9S-03C1B-02A	Two Terminal Blocks (black) with spring-cage terminals, and a set of six code marks to prevent erroneous insertion.
999	Terminal Block with spring- cage terminals (4-pin)	G9SX-NS G9SX-EX-□	Y9S-04C1B-02A	Two Terminal Blocks (black) with spring-cage terminals, and a set of six code marks to prevent erroneous insertion.

Note: The G9SX main unit comes with a terminal block as standard equipment. The accessories shown here can be ordered as a replacement. * The illustrations show 3-pin types

^{*2.} The OFF-delayed output becomes an instantaneous output by setting the OFF-delay time to 0 s.

^{*3.} PNP transistor output

^{*4.} The OFF-delay time can be set in 16 steps as follows: 0/0.2/0.3/0.4/0.5/0.6/0.7/0.8/0.9/1.0/1.2/1.4/1.8/2.0/2.5/3.0 s

^{*2.} The OFF-delay time is synchronized to the OFF-delay time setting in the connected Controller (G9SX-NSA222-T03-□).